

PATENT

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

APPLICANTS: HSU et al.

EXAMINER: TRAN, Andrew Q.

SERIAL NO: 10/673,801

ART UNIT: 2824

FILED: September 29, 2003

DKT: YOR920030217US1(8728-628)

ON: SEGMENTED CONTENT ADDRESSABLE MEMORY ARCHITECTURE
FOR IMPROVED CYCLE TIME AND REDUCED POWER CONSUMPTION

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

RESPONSE TO ADVISORY ACTION

This paper, which is being filed in conjunction with a Request for Continued Examination, is supplemental to Applicants' Amendment filed on November 13, 2006, responsive to the Advisory Action mailed on December 11, 2006, and further responsive to the Final Office Action mailed on September 13, 2006. The Examiner is requested to enter and consider the following remarks.